



UNITED STATES PATENT AND TRADEMARK OFFICE

COMMISSIONER FOR PATENTS  
UNITED STATES PATENT AND TRADEMARK OFFICE  
WASHINGTON, D.C. 20231  
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APPLICATION NUMBER	FILING/RECEIPT DATE	FIRST NAMED APPLICANT	ATTORNEY DOCKET NUMBER
09/900,945	07/10/2001	Toshitada Saito	211200US2

22850  
OBLON SPIVAK MCCLELLAND MAIER & NEUSTADT PC  
FOURTH FLOOR  
1755 JEFFERSON DAVIS HIGHWAY  
ARLINGTON, VA 22202

CONFIRMATION NO. 7956

FORMALITIES LETTER



\*OC000000006461877\*

Date Mailed: 08/23/2001

## NOTICE TO FILE MISSING PARTS OF NONPROVISIONAL APPLICATION

FILED UNDER 37 CFR 1.53(b)

*Filing Date Granted*

An application number and filing date have been accorded to this application. The item(s) indicated below, however, are missing. Applicant is given **TWO MONTHS** from the date of this Notice within which to file all required items and pay any fees required below to avoid abandonment. Extensions of time may be obtained by filing a petition accompanied by the extension fee under the provisions of 37 CFR 1.136(a).

- The oath or declaration is missing.  
*A properly signed oath or declaration in compliance with 37 CFR 1.63, identifying the application by the above Application Number and Filing Date, is required.*

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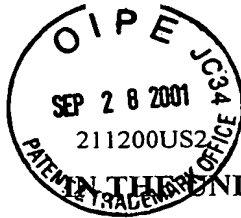
*A copy of this notice **MUST** be returned with the reply.*

Customer Service Center

Initial Patent Examination Division (703) 308-1202

PART 2 - COPY TO BE RETURNED WITH RESPONSE

Docket No.



211200US2

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF: Toshitada SAITO

SERIAL NUMBER: 09/900,945

ATTN: APPLICATION BRANCH

FILING DATE: July 10, 2001

FOR: ONE-CHIP SYSTEM LARGE-SCALE INTEGRATED CIRCUIT INCLUDING PROCESSOR CIRCUIT  
AND ITS PERIPHERAL CIRCUITS

**FILING OF DECLARATION UNDER 37 CFR 1.53(f)**

ASSISTANT COMMISSIONER FOR PATENTS  
WASHINGTON, D.C. 20231

SIR:

Responsive to the notification dated August 23, 2001, and in accordance with the provisions of 37 CFR 1.53(f), Applicant submits herewith a Rule 63 Declaration.

The required fee was paid at the time of filing the application.

In light of the foregoing, this application is deemed to be in proper condition for examination and such favorable action is earnestly solicited.

Respectfully Submitted,

OBLON, SPIVAK, McCLELLAND,  
MAIER & NEUSTADT, P.C.

Marvin J. Spivak

Attorney of Record

Registration No. 24,913

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ATTN: APPLICATION BRANCH

**THIS IS A RESPONSE TO A  
NOTICE TO FILE MISSING  
PARTS OF AN APPLICATION**

ASSISTANT COMMISSIONER FOR PATENTS  
WASHINGTON, D.C. 20231

Re: Inventor: Toshitada SAITO  
Serial No: 09/900,945  
Filed: July 10, 2001  
For: ONE-CHIP SYSTEM LARGE-SCALE INTEGRATED CIRCUIT  
INCLUDING PROCESSOR CIRCUIT AND ITS PERIPHERAL  
CIRCUITS

SIR:

Attached hereto for filing are the following papers:

**Notice to File Missing Parts  
Filing of Declaration/Declaration**

Our check in the amount of \$.00 is attached covering any required fees. In the event that any variance exists between the amount enclosed and the Patent Office charges for filing the above-noted documents, including any fees required under 37 CFR 1.136 for any necessary Extension of Time to make the filing of the attached documents timely, please charge or credit the difference to our Deposit Account No. 15-0030. Further, if these papers are not considered timely filed, then a petition is hereby made under 37 CFR 1.136 for the necessary extension of time. A duplicate of this sheet is enclosed.

Respectfully Submitted,

OBLON, SPIVAK, McCLELLAND,  
MAIER & NEUSTADT, P.C.

  
Marvin J. Spivak

Registration No. 24,913

**Joseph A. Scafetta, Jr.  
Registration No. 26,803**

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